

Serial No.: 10/008,451
Group Art Unit: 2819
Examiner: Linh V. Nguyen

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Timaru, et al. Docket: 135916
Serial No.: 10/008,451 Art Unit: 2819
Filed: November 30, 2001 Examiner: Linh V. Nguyen
Title: Predistortion Linearizer and Method for Linearizing a Nonlinear Device

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AMENDMENT

Commissioner for Patents
Washington, D.C. 20231

Certificate Under 37 C.F.R. §1.8(a)

I hereby certify that this correspondence is being transmitted via facsimile to the U.S. Patent Office to 703-872-9318, Attention Examiner Linh V. Nguyen, on January 30, 2003 in accordance with 37 C.F.R. §1.6(d).

Jessica W. Smith
Jessica W. Smith, Reg. No. 39,884

Dear Commissioner:

In response to the Office Action ("Office Action") dated October 30, 2002, please enter the following amendment:

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In the Claims:

TECHNOLOGY CENTER 2800

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1 (Amended). A circuit for amplifying a signal, comprising:
a power amplifier having an input signal path and an output signal path; and
a predistortion linearizer circuit that is capable of generating a distorted signal which is reflected onto the input signal path of the power amplifier and inputted into said power amplifier, wherein said predistortion linearizer is located a predetermined distance from the input signal path and not physically coupled to the input signal path, and said distorted signal compensates for at least some of the nonlinear spurs introduced by said power amplifier to an input signal applied to the input signal path and inputted into said power amplifier such that said power amplifier generates a compensated output signal.

2 (Amended). The circuit of Claim 1, wherein said predistortion linearizer includes:
a diode;
a coupling circuit, coupled to said diode, capable of introducing a relatively small amount of power from the input signal into said diode and further capable of reflecting the distorted